

RTD A2



## RTDs acquisition module

## Description

FLXMOD<sup> $\mathbb{M}$ </sup> system analog module, with two inputs for 100 $\Omega$  standard RTDs having a resolution of 12 bit. This unit, designed for mounting on a standard DIN rail and housed in a compact plastic case, has an insulated front-end circuitry for best accuracy. Proprietary FLXIO<sup> $\mathbb{M}$ </sup> bus ensure reliable real-time communication with the CPU.



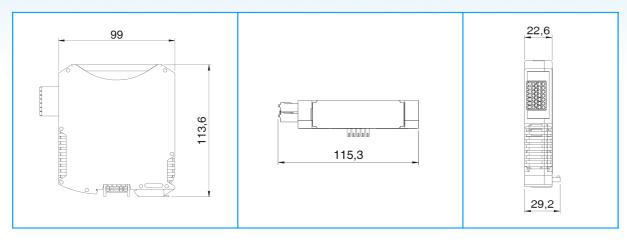
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Technical data	
Housing material	Polypropylene
Degree of protection	IP20
Number of analog inputs	2
Analog inputs characteristics	For two-wire and three-wire $100\Omega$ platinum RTDs
Analog inputs range	79 ÷ 232Ω (-53° ÷ +358°C)
RTD bias current	930μΑ
Analog inputs resolution	12 bit
Interface	Proprietary FLXIO™
Bus address setting	By rotary switch
Electrical specifications	Logic supply: $24V_{DC}$ and $5V_{DC}$ from backplane
Environmental specifications	Operating temperature: from +5°C to +55°C Storage temperature: from -25°C to +85°C Relative humidity: from 10% to 95% (noncondensing

## **Dimensions**



Data here mentioned are subject to changes without any notice.

